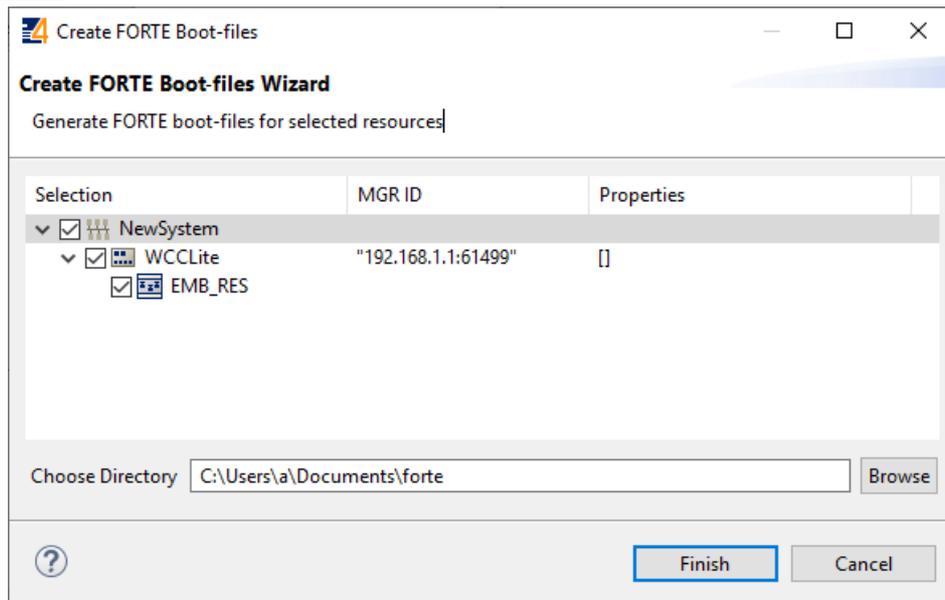


# Generating and uploading FORTE logic file

After the PLC design is finished and debugged, such design can be compiled into FBOOT file and uploaded to one or multiple devices to be used in production. As application being debugged is not automatically considered as a default application, one should be uploaded explicitly via web interface.

To generate FORTE boot-files a user should select **Run->Create FORTE boot-file....** After that one should select devices which should have their boot files created as well as additional devices' properties and directory where these files should be stored as in picture bellow.

Generating FBOOT file:



Upload button for FORTE file in web interface can be found in Protocol Hub tab, Configuration screen (FORTE boot file upload supported for versions v1.4.0 and above). You should see a view as in picture below.

WCC Lite Web interface. Upload and download of 4Diac configuration files:

**Protocol configuration**

**IMPORT PROTOCOL CONFIGURATION**

Here you can import Excel configuration file. Up to 1000 signals are allowed. All previous signals will be replaced.

Configuration file:  No file chosen

PLC (IEC-61499) Boot file:  No file chosen

IEC61850 Server model file:  No file chosen

**DOWNLOAD CONFIGURATION**

Current configuration (IEC61850-server-test (1).xlsx):

Template configuration:

Current IEC 61850 Server model file (Elseta\_SimpleIO.cfg):

 Please be noted that only files with \*.fboot extension are allowed.

After the file has been imported one should be able to download it from the same screen as seen in picture before.

 Uploading file saves its name and shows it in web interface. It is advised to carefully choose filename to separate different versions of PLC application files.

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🕒Revision #1

★Created 14 October 2020 08:00:28 by Raimundas Slaviskas

✎Updated 14 October 2020 08:04:48 by Raimundas Slaviskas